

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Michio NEMOTO et al.

Serial No.: NEW APPLICATION

Group Art Unit:

Filed: April 12, 2004

Examiner:

For: REVERSE BLOCKING SEMICONDUCTOR DEVICE AND A METHOD FOR  
MANUFACTURING THE SAME

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

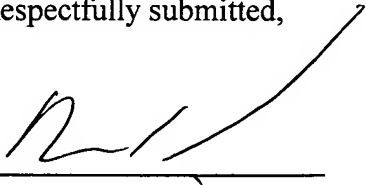
Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. Copies of the references listed on Form PTO-1449 are attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom, and that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

Date: 04/12/04

  
\_\_\_\_\_  
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## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional) FUJI:303

SERIAL NO.: NEW APPLICATION

APPLICANT(s) Michio NEMOTO et al.

FILING DATE: April 12, 2004

Group Art Unit

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US	6,091,086	7/00	Zommer	247	121	
	US	5,698,454	12/97	Zommer	437	6	
	US	5,608,237	3/97	Aizawa et al.	257	132	

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	JP	6-97469 A	4/94	JAPAN (English Abstract)			X	
	JP	7-307469 A	11/95	JAPAN (English Abstract)			X	
	JP	2001-185727 A	7/01	JAPAN (English Abstract)			X	
	JP	2002-76017 A	3/02	JAPAN (English Abstract)			X	
	JP	2002-353454 A	12/02	JAPAN (English Abstract)			X	
	JP	2002-319676 A	10/02	JAPAN (English Abstract)			X	

## OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

"600V-IGBT with Reverse Blocking Capability"; Takei et al.; Proceedings of 2001 International Symposium on Power Semiconductor Devices & ICs, Osaka, Japan; pgs. 413-416.

EXAMINER DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.